This listing of claims will replace all prior versions, and listings of claims in the application:

LISTING OF CLAIMS:

1.   (Currently amended) A multiple stage processing pipeline for handling bit streams encoded in accordance with different standards and arranged as a single serial bit stream, the multiple stage processing pipeline comprising:

a token generator responsive to the single serial bit stream for generating at least one token[[s]]; [[and]]

at least one reconfigurable processing stage to receive said at least one token; and

a parser in said at least one reconfigurable processing stage configured to respond to [[the generated]] said at least one token[[s]] by enabling for processing different portions of the single serial bit stream corresponding to different ones of the different standards.

2.   (Currently amended) The multiple stage processing pipeline of claim 1, wherein the at least one reconfigurable processing stage comprises:

a token decoder for recognizing [[ones of]] the [[generated]] token[[s]] as a control token[[s]] pertinent to that at least one reconfigurable processing stage and for passing said token to another of the stages if said token is an unrecognized one[[s of the generated tokens to another of the stages]].

3.   (Currently amended) The multiple stage processing pipeline of claim 2,
wherein the at least one reconfigurable processing stage further comprises:

        an action identification unit responsive to [[at least one of]] the control
token[[s]] for reconfiguring the at least one reconfigurable processing stage to
process a data token identified by the at least one [[control]] token according to one
of the different standards..

4.   (Original) The multiple stage processing pipeline of claim 3, wherein the
token generator, the action identification unit and the token decoder are implemented
in hardware.

5.   (Original) The multiple stage processing pipeline of claim 1, wherein
the different standards include MPEG.

6.   (Original) The multiple stage processing pipeline of claim 1, wherein the
different standards include JPEG.

7.   (Original) The multiple stage processing pipeline of claim 1, wherein the
different standards include H.261.

8.   (Original) The multiple stage processing pipeline of claim 1, wherein the
at least one reconfigurable processing stage comprises a spatial decoding stage.

9.   (Original) The multiple stage processing pipeline of claim 1, wherein the
at least one reconfigurable processing stage comprises a temporal decoding stage.

10.   (Original) The multiple stage processing pipeline of claim 1, wherein the at least one reconfigurable processing stage comprises reconfigurable processing stages including at least one spatial decoding stage and at least one temporal decoding stage.

11.   (Original) The multiple stage processing pipeline of claim 9, wherein the temporal decoding stage comprises a reconfigurable prediction filters block.

12.   (Currently amended) The multiple stage processing pipeline of claim 11, wherein the [[generated]] token comprises a coding standard token and wherein the reconfigurable prediction filters block is reconfigured in response to the coding standard token.

13.   (Currently amended) The multiple stage processing pipeline of claim 12, wherein the [[generated]] token comprises a prediction mode token and wherein the reconfigurable prediction filters block operates in accordance with the mode specified in the prediction mode token.

14.   (Currently amended) A method for handling bit streams encoded in accordance with different standards and arranged as a single serial bit stream comprising:

   generating tokens in response to the single serial bit stream;

   recognizing certain of the generated tokens as control tokens;

   parsing the control tokens to reconfigure the at least one reconfigurable processing stage; and

processing different portions of the single serial bit stream <u>in the</u> <u>reconfigurable processing stage</u> corresponding to different ones of the standards in response to the generated tokens.

15. (cancelled)

16. (cancelled)

17.   (Original) The method of claim 14, wherein the different standards include JPEG.

18.   (Original) The method of claim 14, wherein the different standards include MPEG.

19.   (Original) The method of claim 14, wherein the different standards include H.261.

20.   (Currently amended) A system comprising:

a detector unit for receiving a data stream of data having portions encoded according to different standards and for generating tokens based on respective portions of the received data stream;

<u>a parser to process the tokens and to reconfigure at least one reconfigurable</u> <u>processing stage in response to the token;</u> and

a processor configured to respond to the generated tokens by processing the respective portions of the [[single serial bit]] <u>data</u> stream corresponding to different

standards to produce a decoded output <u>using the processing stage reconfigured by the parser</u>.

21.   (Original) The system of claim 20, wherein the processor comprises a pipeline processor having at least one reconfigurable processing stage.

22.   (Original) The system of claim 21, further comprising:

a token decoder for recognizing ones of the generated tokens as control tokens pertinent to that at least one reconfigurable processing stage.

23.   (Original) The system of claim 21, wherein the at least one reconfigurable processing stage comprises:

an action identification unit responsive to at least one of the control tokens for reconfiguring the at least one reconfigurable processing stage to process a data token identified by the at least one control token according to one of the different standards.

24.   (Original) The system of claim 20, wherein the different standards include MPEG.

25.   (Original) The system of claim 20, wherein the different standards include JPEG.

26.   (Original) The system of claim 20, wherein the different standards include H.261.